

Fig. 1

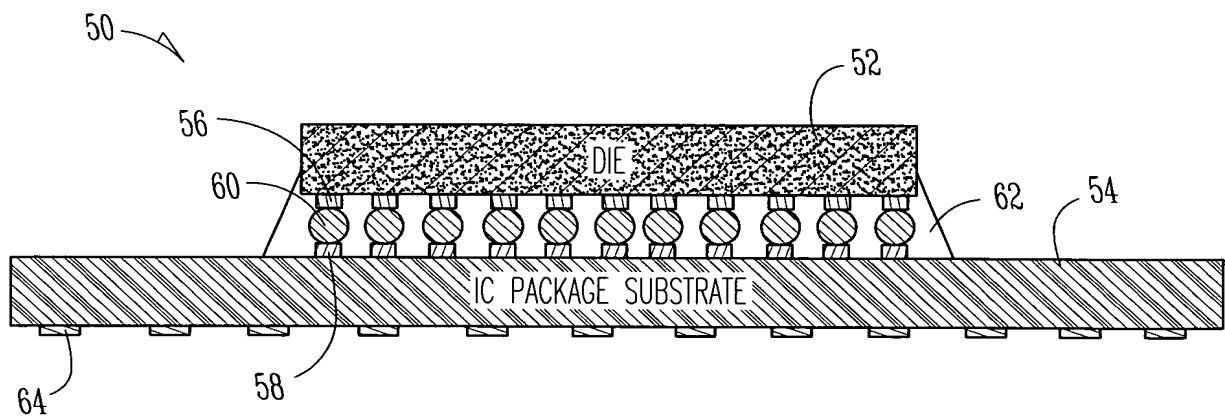


Fig. 2
(Prior Art)



TITLE: ELECTRONIC PACKAGES HAVING MULTIPLE-ZONE INTERCONNECTS AND METHODS OF MANUFACTURE
INVENTORS NAME: Gilroy J. Vandentop et al.
SERIAL NO.: 10/004002

2/6

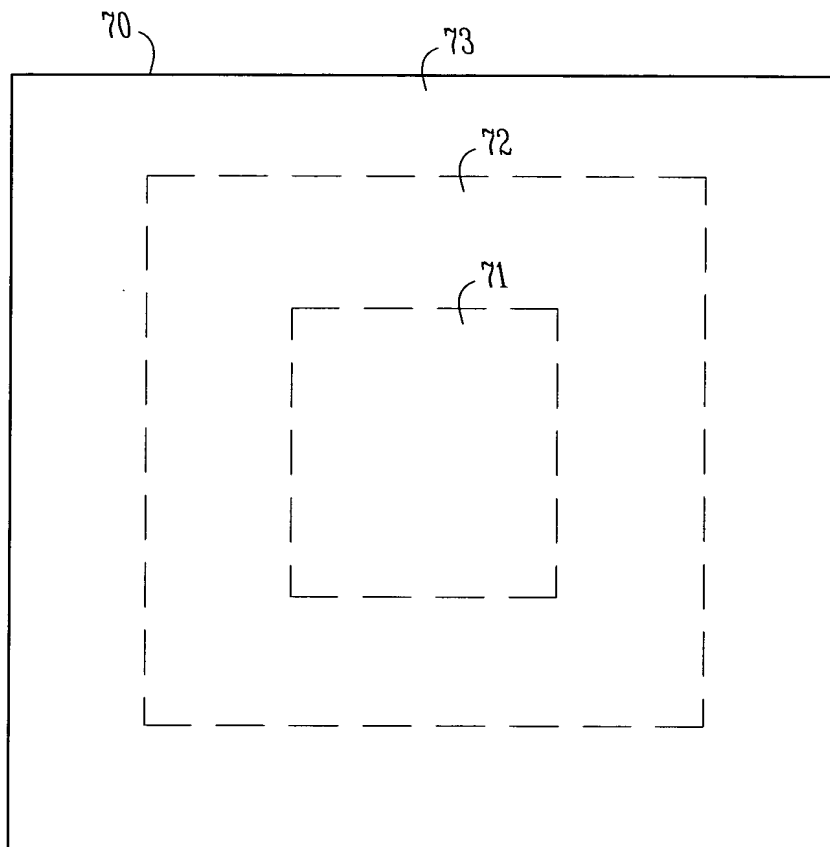


Fig. 3



TITLE: ELECTRONIC PACKAGES HAVING MULTIPLE-ZONE INTERCONNECTS AND METHODS OF MANUFACTURE

INVENTORS NAME: Gilroy J. Vandentop et al.

SERIAL NO.: 10/004002

3/6

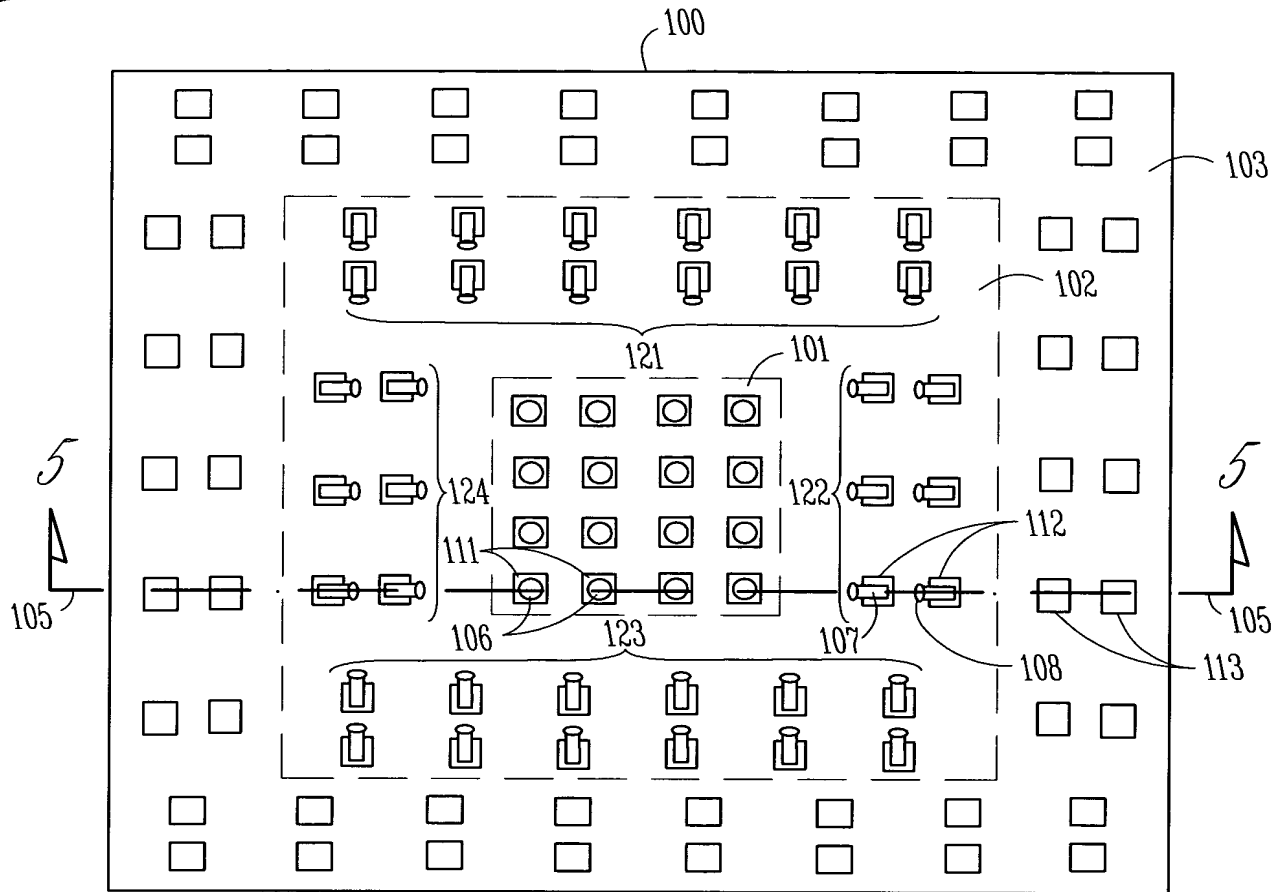


Fig. 4

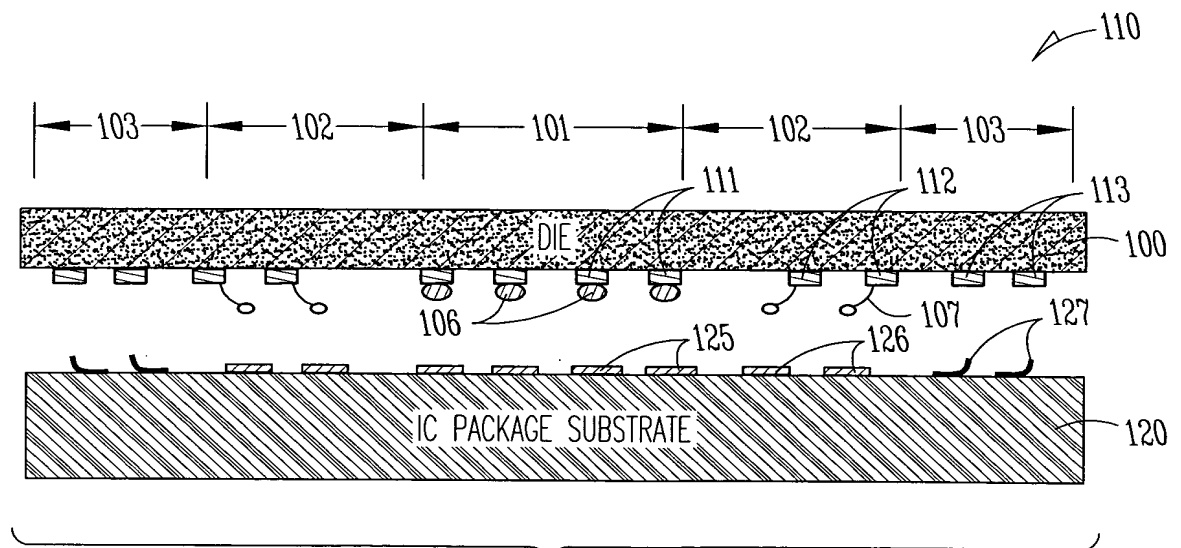


Fig. 5

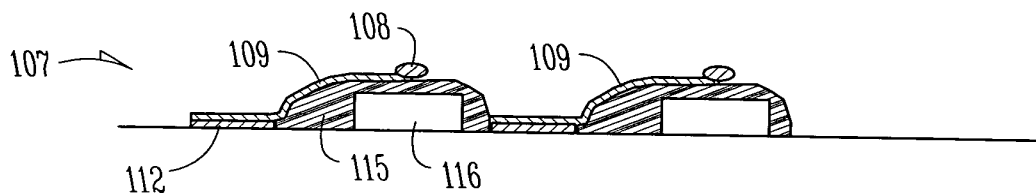


Fig. 6

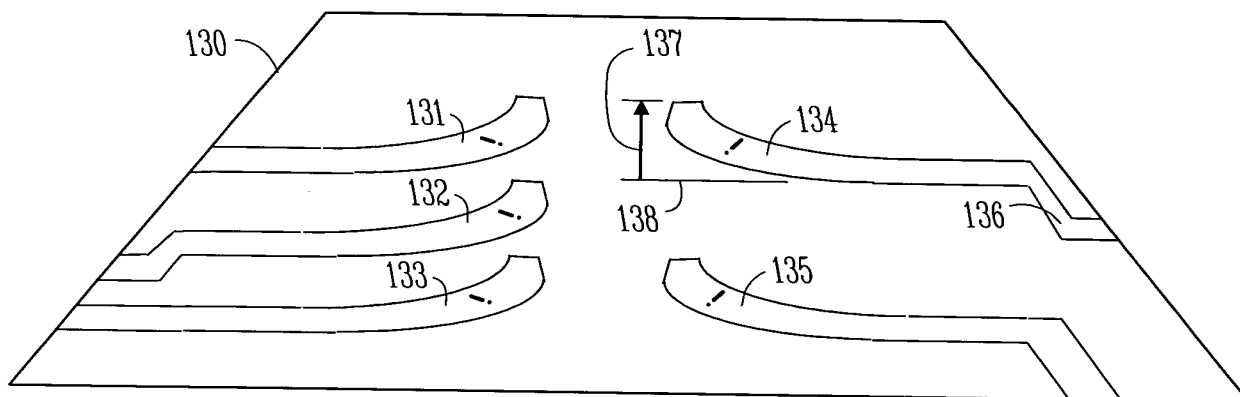


Fig. 7

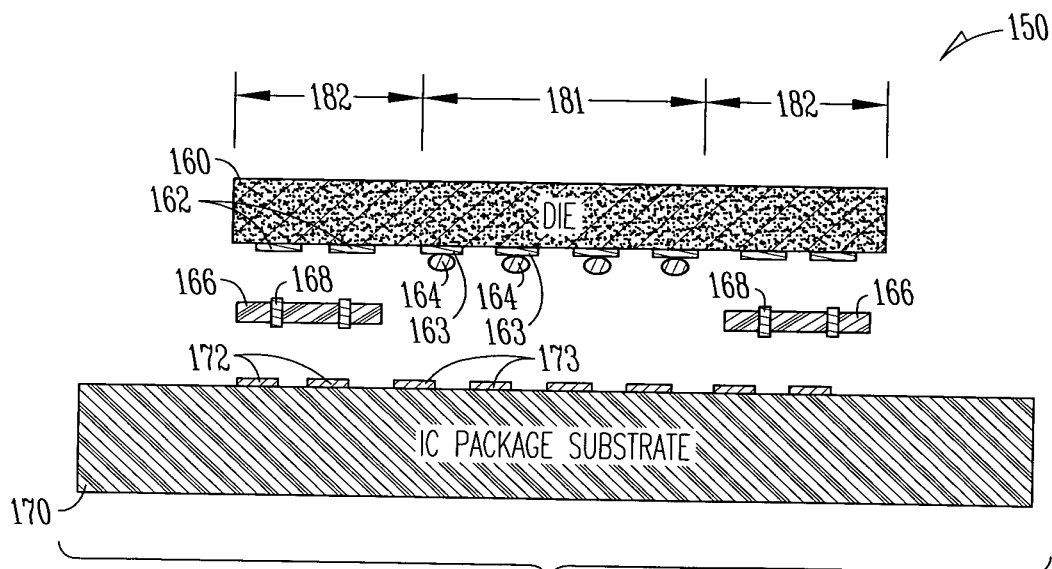


Fig. 8

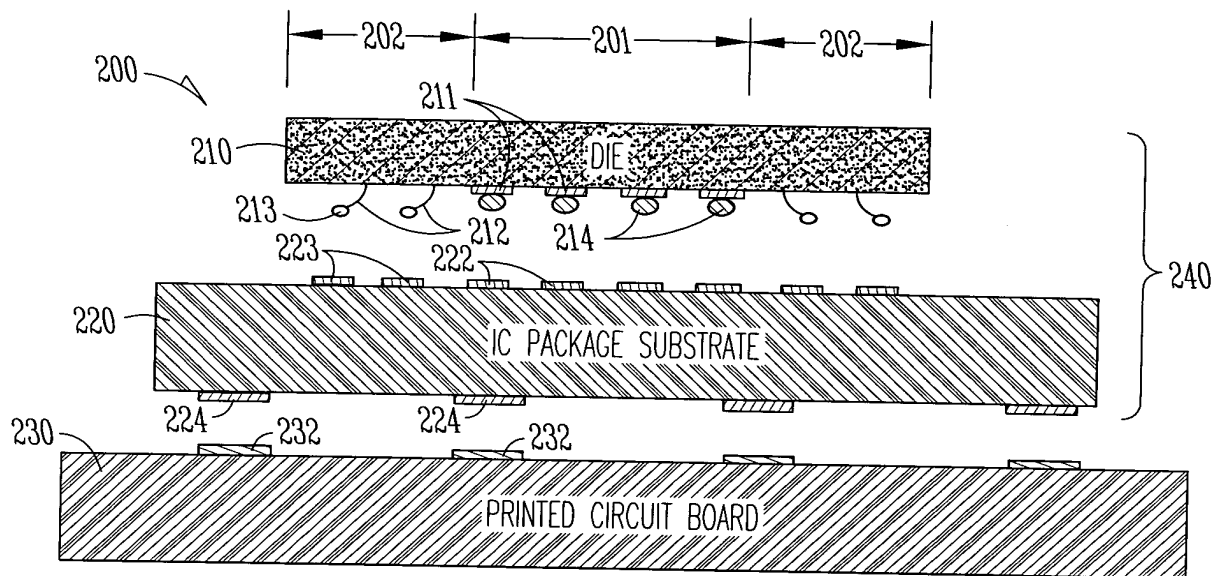


Fig. 9

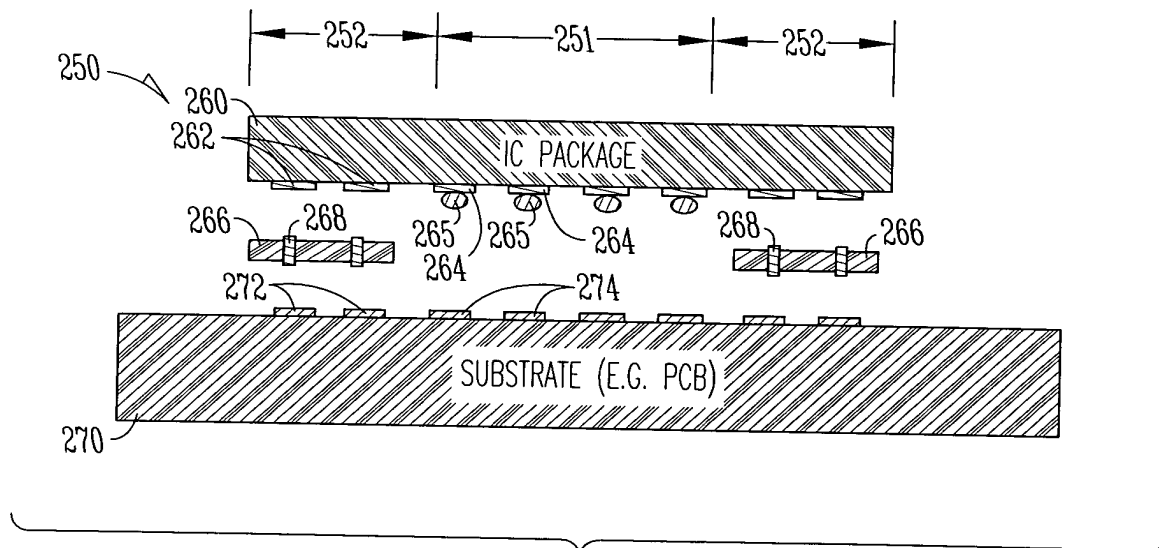


Fig. 10

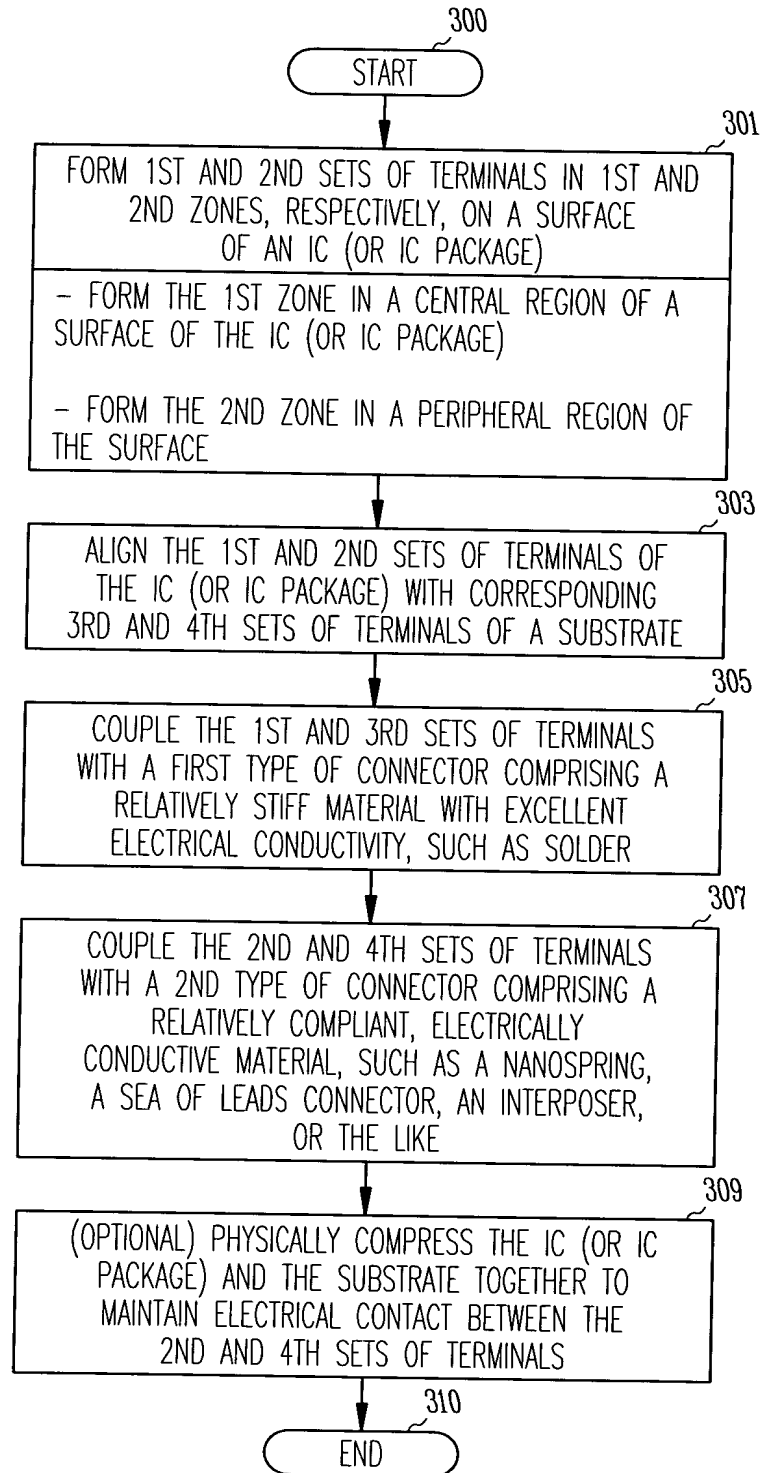


Fig. 11